

DRAWINGS:

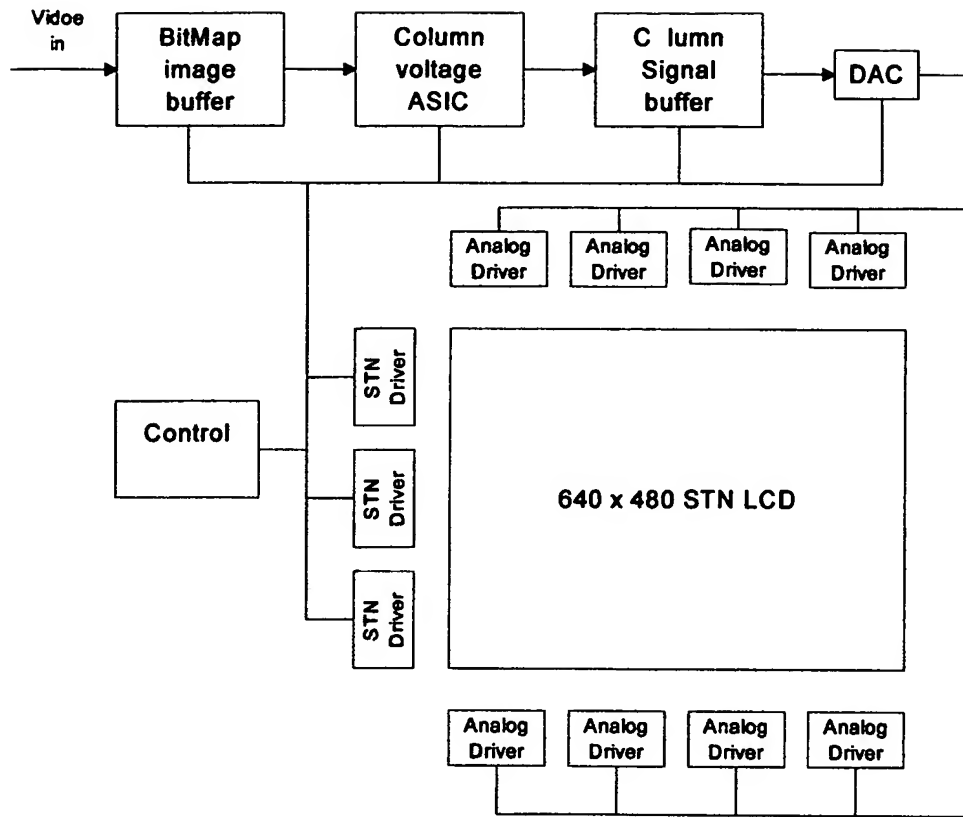


Fig. 1. The block diagram of STN row and column control for PHM display.

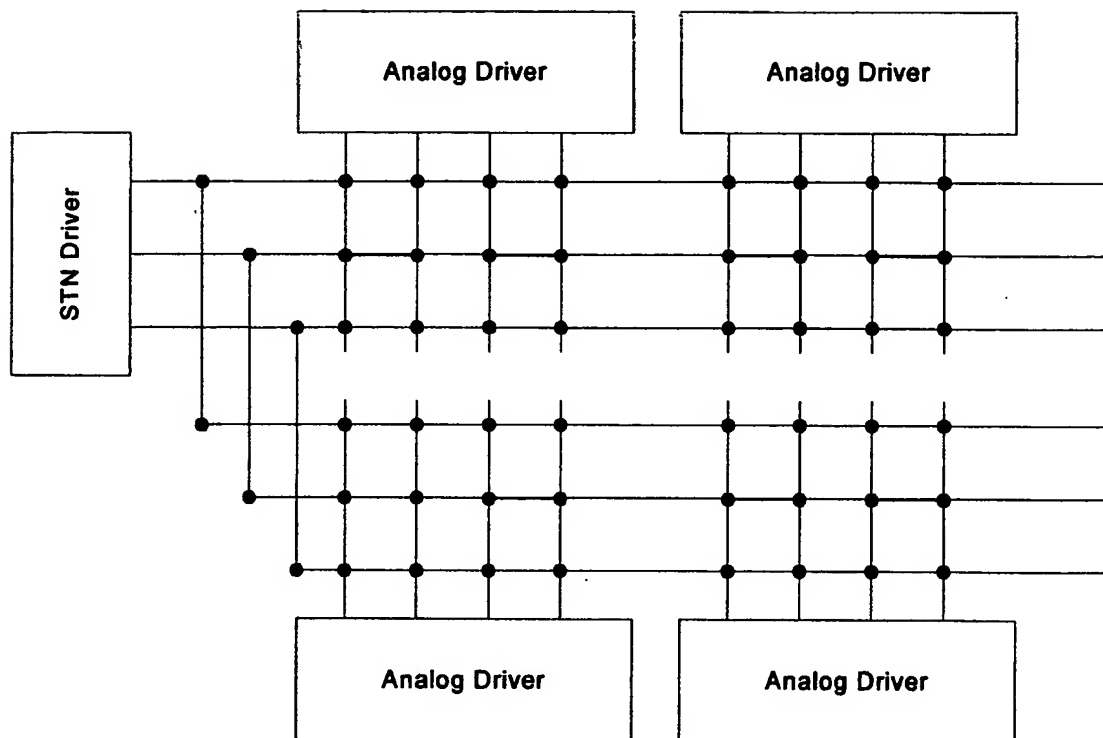


Fig. 2. The diagram of a simplified Passive Matrix Addressing of TN Display with dual scans scheme.

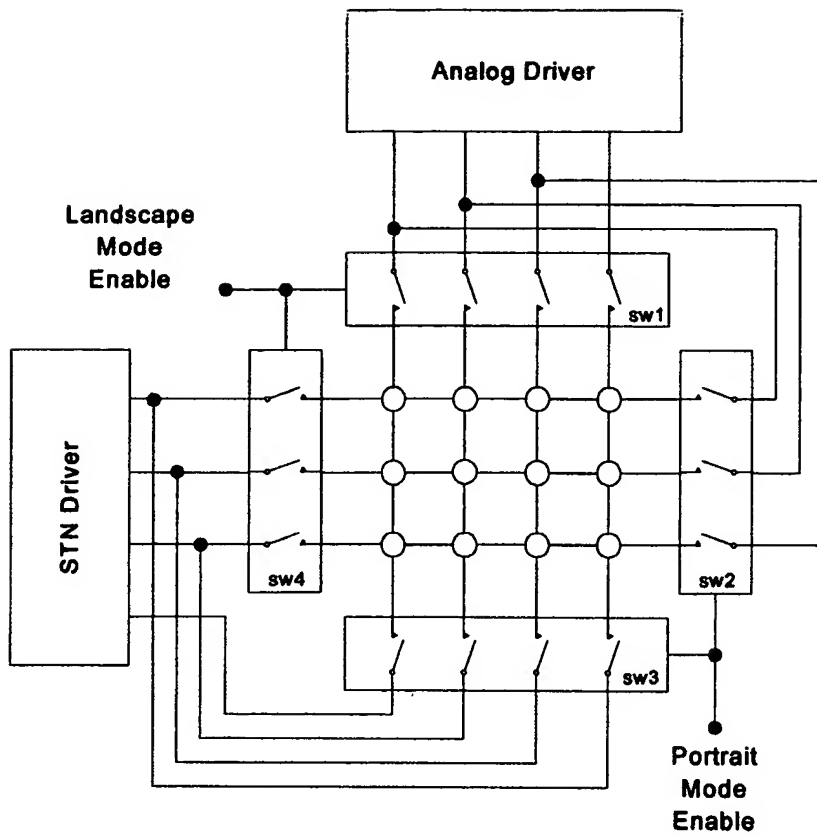


Fig. 3. The diagram of the extra scan line scheme.

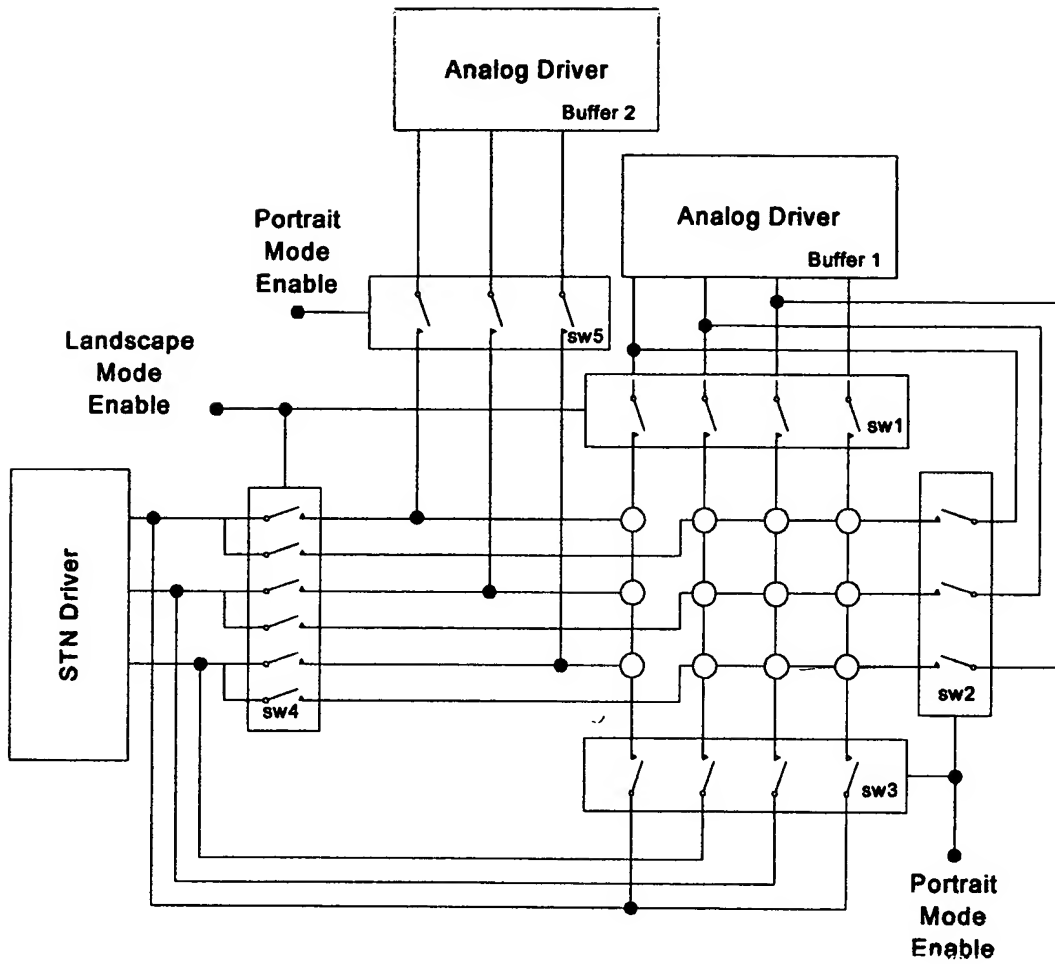


Fig. 4. The diagram of the extra column buffer scheme.

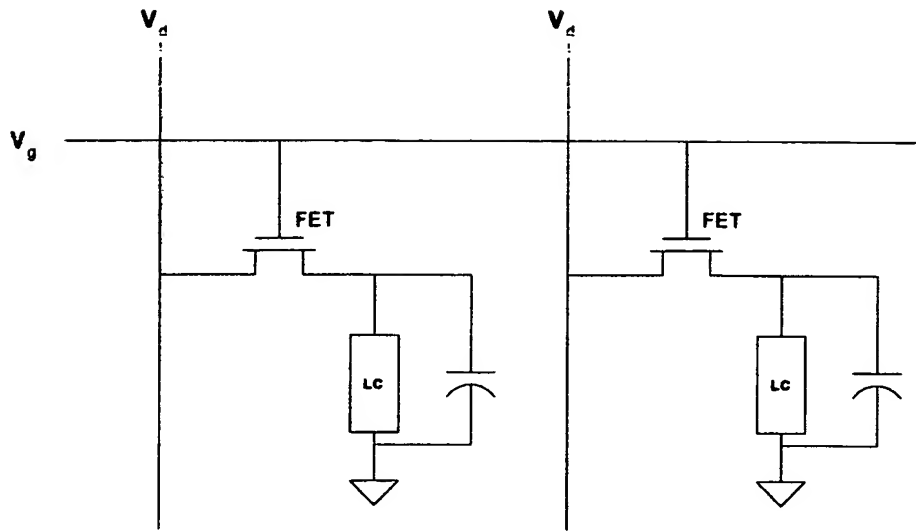


Fig. 5. Simplified Row in the TFT LCD Panel

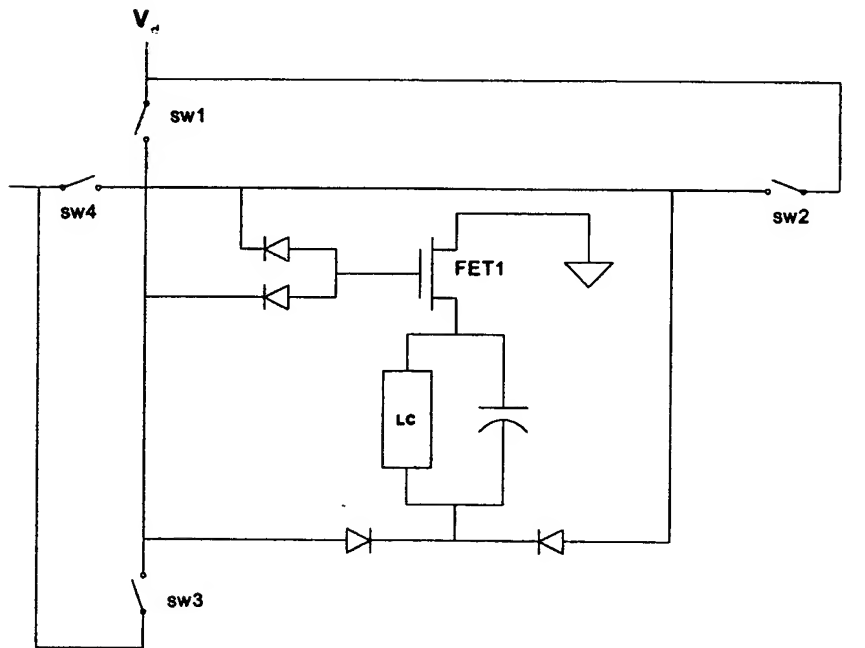


Fig. 6. The Common Source Circuit method.

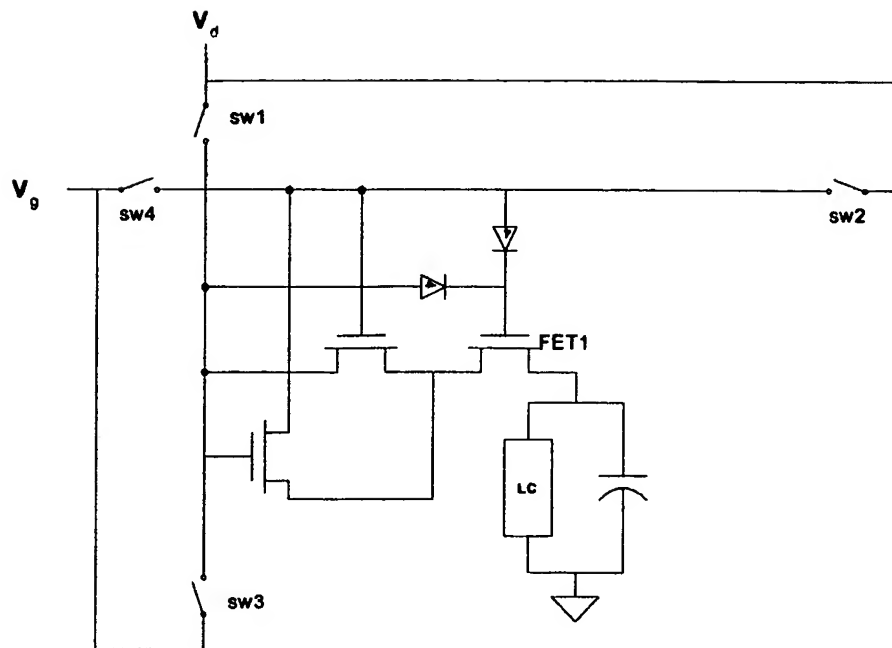


Fig. 7. The Common Drain Circuit method.